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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Richard L. Coulson et al.

Art Unit:

2863

Serial No.:

10/660,310

Examiner:

Aditya S. Bhat

Filed:

September 11, 2003

Docket:

ITL.1029US

P16765

For:

Adaptive Cache Algorithm for

Temperature Sensitive Memory

Assignee:

Intel Corporation

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

REPLY TO FINAL REJECTION

Sir:

In response to the final rejection mailed October 25, 2005, please amend the abovereferenced patent application as follows:

December 2, 2005

certify that this document is being facsimile transmitted to United/States Patent and Trademark Office (Fax No. 571/273-